**BUFFER ISSUE RESOLUTION DOCUMENT (BIRD)**

**BIRD NUMBER:**

**ISSUE TITLE:** DQ\_DQS GetWave Flow for Clock Forwarding Modeling

**REQUESTOR:**  Walter Katz, The MathWorks

 Fangyi Rao, Keysight

 Ambrish Varma, Cadence

**DATE SUBMITTED:**

**DATE REVISED:**

**DATE ACCEPTED:**

**DEFINITION OF THE ISSUE:**

In DDR systems, DQ Rx uses DQS Rx output signal as a forwarded clock to clock the slicer and the DFE. To model such clock forwarding functionality, the DQ Rx model needs to input DQS clock time. This BIRD proposes a new AMI Reserved Parameters to enable EDA tool to input clock times from the DQS Rx GetWave function to the DQ Rx GetWave function.

DQ and DQS AMI models will be delivered as a match pair. The model maker can put all the DQS path delays in the DQS AMI\_GetWave or in the DQS path delays that is in the DQ AMI\_GetWave or split the DQS path delays between the DQ and DQS AMI\_GetWave. There are several scenarious that demonstrate this:

1. The path delay from the DQS pad to all DQ latches in the chip are the same.in a x4 lane and the same in a x8 lane, but the x4 path delay is different than a x8 path delay. In this case the x4 DQ and the x8 DQ would have the same DLL. There would be a different DQS DLL for the x4 and x8 chip.
2. The path delay from the DQS pad to all DQ latches in the chip are different. In this case there would be one DQS DLL which had a common path delay (either 0. Or some common path delay for all bits and each bit would have a different DLL, (or more likely a different .ami file which a a common DQ DLL) that would accommodate the different DQS path delay for each bit.

Since this clock forwarding can be used for other applications, the words Clock and Data will be used in the BIRD in lieu of DQS and DQ.

**SOLUTION REQUIREMENTS:**

The IBIS specification must meet these requirements:

Table 1: Solution Requirements

|  |  |
| --- | --- |
| Requirement | Notes |
| 1. Allow the Rx model to model clock forwarding.
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**SUMMARY OF PROPOSED CHANGES:**

Add new Reserved Parameters Rx\_Clock and Rx\_Use\_Clock\_output

**PROPOSED CHANGES:**

*Parameter:* **Rx\_Use\_Clock\_Input**

*Required:* No, and illegal before AMI\_Version 7.1

*Direction:* Rx

*Descriptors:*

Usage:                   In

Type:                     Integer

Format:                 (List “None” “Times” “Wave”)

Default:                 *<*string\_literal>

Description:*<*string>

*Definition:* Tells the EDA tool if the Rx Data AMI\_GetWave clock\_times input is empty or contains the \*clock\_times or \*wave output of the Rx Clock AMI\_GetWave. The list of values must always contain “None”. The list shall also contain “Times” and/or “Wave”.

*Usage Rules:* If omitted, the default is “None”. If “None” is selected, then the content of \*clock\_times will be ignored by the model. If “Times” is selected, then the EDA tool will use the \*clock\_times pointer that was output by the Rx Clock AMI\_GetWave call as the \*clock\_times pointer in the call to the Data AMI\_GetWave. If “Wave” is selected, then the EDA tool will use the \*wave pointer that was output be the Rx Clock AMI\_GetWave call as the \*clock\_times pointer in the call to the Data AMI\_GetWave.

*Other Notes:* The \*wave input to both Data and Clock shall have the same block size and sample\_interval. If the Clock does not have a DLL or has a DLL without a AMI\_GetWave then the EDA tool should effectively insert a passthrough Clock GetWave function to create the \*wave or \*clock\_times output vector required by the Data AMI\_GetWave \*clock\_times input.

*Example:*

(Rx\_Use\_Clock\_output (Usage In) (List “None” “Times”)(Type String)

(Description "The model can use the Clock AMI\_GetWave output \*clock\_times

 to sample the Data waveform at the Rx Data latch")

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DQ0

DQ Tx DLL

DQS Tx DLL

DQ Tx DLL

DQ7

DQS0

+

-

…

…

DQ8

DQ Tx DLL

DQS Tx DLL

DQ Tx DLL

DQ15

DQS1

DQ0

DQ7

+

-

+

-

DQS0

DQ8

DQS1

DQ15

…

…

…

…

DQS Rx DLL

DQ Rx

\*clock\_times

DQ Rx

\*clock\_times

DQS Rx DLL

DQ Rx

\*clock\_times

DQ Rx

\*clock\_times

1

1

1

1

1

1

2

2

3

3

3

3

Step 1: compute analog channel output according to IBIS 5.1-7.0 (crosstalk taken into account)

Step 2: compute output of all DQS Rx DLLs according to IBIS 5.1-7.0

Set DQ \*clock\_times pointer to either DQS \*clock\_times or \*wave pointer

Step 3: compute output of all DQ Rx DLLs

Channel

**BACKGROUND INFORMATION/HISTORY:**